

<b>Notice of References Cited</b>	Application/Control No. 09/548,469	Applicant(s)/Patent Under Reexamination SINHAROY ET AL.	
	Examiner Aimee J Li	Art Unit 2183	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,642,493	06-1997	Burgess, Bradley	711/116
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel. Pentium® Processor Family Developer's Manual Volume 3: Architecture and Programming Manual. Intel Corporation © 1995. Pages 3-13 to 3-15.
	V	Hennessy, John L. and Patterson, David A. Computer Architecture A Quantitative Approach. Second Edition. San Francisco, California: Morgan Kaufmann Publishers, Inc. ©1996. Pages 175-177 and 324-326.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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